Charting the Complexity Landscape of Virtual Network Embeddings

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Abstract—Many resource allocation problems in the cloud can be described as a basic Virtual Network Embedding Problem (VNEP): the problem of finding a mapping of a request graph (describing a workload) onto a substrate graph (describing the physical infrastructure). Applications range from mapping testbeds (from where the problem originated), over the embedding of batch-processing workloads (virtual clusters) to the embedding of service function chains. The different applications come with their own specific requirements and constraints, including node mapping constraints, routing policies, and latency constraints. While the VNEP has been studied intensively over the last years, complexity results are only known for specific models and we lack a comprehensive understanding of its hardness.

This paper charts the complexity landscape of the VNEP by providing a systematic analysis of the hardness of a wide range of VNEP variants, using a unifying and rigorous proof framework. In particular, we show that the problem of finding a feasible embedding is \(\mathcal{NP}\)-complete in general, and, hence, the VNEP cannot be approximated under any objective, unless \(\mathcal{P} = \mathcal{NP}\) holds. Importantly, we derive \(\mathcal{NP}\)-completeness results also for finding approximate embeddings, which may violate, e.g., capacity constraints by certain factors. Lastly, we prove that our results still pertain when restricting the request graphs to planar or degree-bounded graphs.

I. INTRODUCTION

At the heart of the cloud computing paradigm lies the idea of efficient resource sharing: due to virtualization, multiple workloads can co-habit and use a given resource infrastructure simultaneously. Indeed, cloud computing introduces great flexibilities in terms of where workloads can be mapped. At the same time, exploiting this mapping flexibility poses a fundamental algorithmic challenge. In particular, in order to provide predictable performance, guarantees on all, i.e. node and edge, resources need to be ensured. Indeed, it has been shown that cloud application performance can suffer significantly from interference on the communication network [1].

The underlying algorithmic problem is essentially a graph theoretical one: both the workload as well as the infrastructure can be modeled as graphs. The former, the so-called request graph, describes the resource requirements both on the nodes (e.g., the virtual machines) as well as on the interconnecting network. The latter, the so-called substrate graph, describes the physical infrastructure and its resources (servers and links). Figure 1 depicts an example of embedding a request graph.

The problem is known in the networking community under the name Virtual Network Embedding Problem (VNEP) and

Figure 1. Example request (left) together with an exemplary embedding on a substrate network (right). The numeric labels at the network elements denote the resource demands (of the request) and the available capacity (of the substrate), respectively. In the embedding, the single request edge \((C, D)\) (green) is realized via a path of length two in the substrate. As request nodes \(A\) and \(C\) are collocated on the same substrate node, the edge \((A, C)\) does not use any substrate edges and hence uses no edge resources. Note that the allocations induced by the embedding do not exceed the substrate’s capacities.

has been studied intensively for over a decade [2], [3]. Besides the rather general study of the VNEP, which emerged originally from the study of testbed provisioning, essentially the same problems are considered in the context of Service Function Chaining [4], [5], as well as in the context of embeddings Virtual Clusters, a specific batch processing request abstraction [6], [7].

A. Related Work

a) Objectives & Restrictions: Depending on the setting, many different objectives are considered for the VNEP. The most studied ones concern minimizing the (resource allocation) cost [2], [3], maximizing the profit by exerting admission control [8], [9], and minimizing the maximal load [4], [10].

Besides commonly enforcing that the substrate’s physical capacities on servers and edges are not exceeded to provide Quality-of-Service [3], additional restrictions have emerged:

• Restrictions on the placement of virtual nodes first arose to enforce closeness to locations of interest [2], but were also used in the context of privacy policies to restrict mappings to certain countries [11]. However, these restrictions are now also used in the context of Service Function Chaining, as specific functions may only be mapped on x86 servers, while firewall appliances cannot [4], [5].

• Routing restrictions first arose in the context of expressing security policies, as for example some traffic may not be
routed via insecure domains or physical links shall not be shared with other virtual networks [3], [12].

- Restrictions on latencies were studied for the VNEP in [13] and have been recently studied intensely in the context of Service Function Chaining to achieve responsiveness and Quality-of-Service [4], [5].

b) Algorithmic Approaches: Several dozens of algorithms were proposed to solve the VNEP and its siblings, including the Virtual Cluster Embedding [6] and Service Function Chain Embedding problem [3]. Most approaches to solve the VNEP either rely on heuristics [2] or metaheuristics [3]. On the other hand, several works study exact (non-polynomial time) algorithms to solve the problem to (near-)optimality or to devise heuristics. Mixed Integer Programming is the most widely used exact approach [4], [9], [13].

Only recently, approximation algorithms providing quality guarantees for the VNEP have been presented. In particular, the embedding of chains is approximated under assumptions on the requested resources and the achievable benefit in [14]. In [15] approximations for cactus request graphs are detailed, while [16] presents fixed-parameter tractable approximations for arbitrary request graph topologies.

c) Complexity Results: Surprisingly, despite the relevance of the problem and the large body of literature, the complexity of the underlying problems has not received much attention. While it can be easily seen that the Virtual Network Embedding Problem encompasses several \(N^P\)-hard problems as e.g. the \(k\)-disjoint paths problem [17], the minimum linear arrangement problem [18], or the subgraph isomorphism problem [19], most works on the VNEP cite a \(N^P\)-hardness result contained in a technical report from 2002 by Andersen [20]. The only other work studying the computational complexity is one by Alamaldi et al. [21], which proved the \(N^P\)-hardness and inapproximability of the profit maximization objective while not taking into account latency or routing restrictions and not considering the hardness of embedding a single request.

Bibliographic Note: An extended version of this paper was published as a technical report [22]. In addition to the contents found in this paper, it contains a detailed Integer Programming formulation able to solve the VNEP variants considered in this paper. Furthermore, it contains the proof of Theorem 25, that is omitted here due to space constraints.

B. Contributions and Overview

In this work, we initiate the systematic study of the computational complexity of the VNEP. Taking all the aforementioned restrictions into account, we first compile a concise taxonomy of the VNEP variants in Section II. Then, we present a powerful reduction framework in Section III, which is the base for all hardness results presented in this paper. In particular, we show the following (see also Table I):

- We show the \(N^P\)-completeness of five different VNEP variants in Section IV. For example, we consider the variant only enforcing capacity constraints, but also one in which only node placement and latency restrictions must be obeyed in the absence of capacity constraints.

- We extend these results in Section V and show that the considered variants remain \(N^P\)-complete even when computing approximate embeddings, which may exceed latency or capacity constraints by certain factors.

- Lastly, we show in Section VI that the respective VNEP variants remain \(N^P\)-complete even when restricting substrate graphs to directed acyclic graphs (DAGs) and request graphs to planar, degree-bounded DAGs.

As we are proving \(N^P\)-completeness throughout this paper, the implications of our results are severe. Given the \(N^P\)-completeness of finding any feasible solution, finding an optimal solution subject to any objective is at least \(N^P\)-hard. Furthermore, unless \(P = N^P\) holds, the respective variants cannot be approximated to within any factor.

Table I summarizes our results and is to be read as follows. Any of the five rightmost columns represents a specific VNEP variant. The \(\checkmark\) symbol indicates restrictions that are enforced, while the \(\ast\) symbol indicates restrictions which are not considered. Importantly, enabling a \(\ast\) restriction, does not change the results (cf. Lemma 9). Considering a specific variant, the respective column should be read from top to bottom. Considering for example \(\langle \text{VE} | \ast\rangle\), its \(N^P\)-completeness is shown in Theorem 19 while its inapproximability when relaxing node capacity constraints is shown in Theorem 23. Lastly, all results also hold under the graph restrictions of the two bottom rows.

<table>
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<tr>
<th>Results</th>
<th>(N^P)-completeness and inapproximability under any objective</th>
<th>Section IV</th>
<th>Section V</th>
<th>Section VI</th>
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<td>Thm. 23</td>
<td>Obs. 26</td>
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II. Formal Model

Void now formally introduce the VNEP and its variants.

Notation: The following notation is used throughout this work. We use $[x]$ to denote $\{1,2,\ldots,x\}$ for $x \in \mathbb{N}$. For a directed graph $G = (V, E)$, we denote by $\delta^+(v) \subseteq E$ and $\delta^-(v) \subseteq E$ the outgoing and incoming edges of node $v \in V$.

When considering functions on tuples, we omit the parentheses of the tuple and simply write $f(a,b)$ instead of $f((a,b))$.

A. Basic Problem Definition

We refer to the physical network as substrate network and model it as directed graph $G_S = (V_S, E_S)$. Capacities in the substrate are given by the function $c_S : V_S \cup E_S \to \mathbb{R}_{\geq 0} \cup \{\infty\}$. The capacity $c_S(u)$ of node $u \in V_S$ may represent for example the number of CPUs while the capacity $c_S(u,v)$ of edge $(u,v) \in E_S$ represents the available bandwidth. By allowing to set substrate capacities to $\infty$, the capacity constraints on the respective substrate elements can be effectively disabled.

We denote by $P_S$ the set of all simple paths in $G_S$.

A request is similarly modeled as directed graph $G_r = (V_r, E_r)$ together with node and edge capacities (demands) $c_r : V_r \cup E_r \to \mathbb{R}_{\geq 0}$.

The task is to find a mapping of request graph $G_r$ on the substrate graph $G$, i.e. a mapping of request nodes to substrate nodes and a mapping of request edges to paths in the substrate. Virtual nodes and edges can only be mapped on substrate nodes and edges of sufficient capacity. Accordingly, we denote by $V^r_S = \{u \in V_S | c_S(u) \geq c_r(i)\}$ the set of substrate nodes supporting the mapping of node $i \in V_r$, and by $E^r_S = \{(u,v) \in E_S | c_S(u,v) \geq c_r(i,j)\}$ the substrate edges supporting the mapping of virtual edge $(i,j) \in E_r$.

Definition 1 (Valid Mapping). A valid mapping of request $G_r$ to the substrate $G_S$ is a tuple $m = (m_V, m_E)$ of functions that map nodes and edges, respectively, s.t. the following holds:

- The function $m_V : V_r \to V_S$ maps virtual nodes to suitable substrate nodes, such that $m_V(i) \in V^r_S$ holds for $i \in V_r$.
- The function $m_E : E_r \to P_S$ maps virtual edges $(i,j) \in E_r$ to simple paths in $G_S$ connecting $m_V(i)$ to $m_V(j)$, such that $m_E(i,j) \subseteq E^r_S$ holds for $(i,j) \in E_r$.

Considering the above definition, note the following. Firstly, the mapping $m_E(i,j)$ of the virtual edge $(i,j) \in E_r$ may be empty, if (and only if) $i$ and $j$ are mapped on the same substrate node. Secondly, the definition only enforces that single resource allocations do not exceed the available capacity. To enforce that the cumulative allocations respect capacities, we introduce the following:

Definition 2 (Allocations). We denote by $A_m(x) \in \mathbb{R}_{\geq 0}$ the resource allocations induced by valid mapping $m = (m_V, m_E)$ on substrate element $x \in G_S$ and define

\[
A_m(u) = \sum_{i \in V_r : m_V(i) = u} c_r(i)
\]

\[
A_m(u,v) = \sum_{(i,j) \in E_r : m_V(i) = u, m_E(i,j) \subseteq E^r_S} c_r(i,j)
\]

for node $u \in V_S$ and edge $(u,v) \in E_S$, respectively.

We call a mapping feasible, if the (cumulative) allocations do not exceed the capacity of any substrate element:

Definition 3 (Feasible Embedding). A mapping $m$ represents a feasible embedding, if the allocations do not exceed the capacity, i.e. $A_m(x) \leq c_S(x)$ holds for $x \in G_S$.

In this paper we study the decision variant of the VNEP, asking whether there exists a feasible embedding:

Definition 4 (VNEP, Decision Variant). Given is a single request $G_r$ that shall be embedded on the substrate graph $G_S$. The task is to find a feasible embedding or to decide that no feasible embedding exists.

B. Variants of the VNEP & Nomenclature

As discussed when reviewing the related work in Section I-A, additional requirements are enforced in many settings. Accordingly, we now formalize (i) node placement, (ii) edge routing, and (iii) latency restrictions. Node placement and edge routing restrictions effectively exclude potential mapping options for nodes and edges. For latency restrictions we introduce latency bounds for each of the virtual edges.

Definition 5 (Node Placement Restrictions). For each virtual node $i \in V_r$ a set of forbidden substrate nodes $\overline{V}^i_S \subseteq V_S$ is provided. Accordingly, the set of allowed nodes $V^i_S$ is defined to be $\{u \in V_S \setminus \overline{V}^i_S | c_S(u) \geq c_r(i)\}$.

Definition 6 (Routing Restrictions). For each virtual edge $(i,j) \in E_r$ a set of forbidden substrate edges $\overline{E}^i_S \subseteq E_S$ is provided. Accordingly, the set of allowed edges $E^i_S$ is set to be $\{(u,v) \in E_S \setminus \overline{E}^i_S | c_S(u,v) \geq c_r(i,j)\}$.

Definition 7 (Latency Restrictions). For each substrate edge $e \in E_S$ the edge’s latency is given via $l_S(e) \in \mathbb{R}_{\geq 0}$. Latency bounds for virtual edges are specified via the function $l_r : E_r \to \mathbb{R}_{\geq 0} \cup \{\infty\}$, such that the latency along the substrate path $m_E(i,j)$, used to realize the edge $(i,j)$, is less than $l_r(i,j)$. Formally, the definition of feasible embeddings (cf. Definition 3) is extended by including that $\sum_{e \in m_E(i,j)} l_S(e) \leq l_r(i,j)$ holds for $(i,j) \in E_r$.

We introduce the following taxonomy to denote the different problem variants.

Definition 8 (Taxonomy). We use the notation $\langle C | A \rangle$ to indicate whether and which of the capacity constraints $C$ and which of the additional constraints $A$ are enforced.

- $\langle \cdot | \cdot \rangle$ denotes by $V$ node capacities, by $E$ edge capacities, and
- $\cdot$ that none are used. When node or edge capacities are not considered, we assume the capacities of the respective substrate elements to be $\infty$.

Considering the additional restrictions, the abbreviations $\cdot$, $N, L, \text{ and } R$ stand for no restrictions, node placement, latency, and routing restrictions, respectively.

Accordingly, $\langle \text{VE} | \cdot \rangle$ indicates the classic VNEP without additional constraints while obeying capacities and $\langle \cdot | NL \rangle$ indicates the combination of node placement and latency
restrictions while neither considering node or edge capacities. We note that the introduction of more restrictions only makes the respective problem harder:

**Lemma 9.** A VNEP variant \( \langle A \mid C \rangle \) that encompasses all restrictions of \( \langle A' \mid C' \rangle \) is at least as hard as \( \langle A' \mid C' \rangle \).

**Proof.** The capacity constraints as well as the additional requirements are all formulated in such a fashion that any one of these can be disabled. Considering capacities and latencies, one may set the respective substrate capacities to \( \infty \) and the latencies of substrate edges to 0, respectively. For node placement and edge restrictions one may set the forbidden node and edge sets to the empty set. Hence, a trivial reduction of capacities etc. are introduced in the respective reductions.

**C. Relaxing Constraints: Approximate Embeddings**

Within this work, we show the VNEP to be \( \mathcal{NP} \)-complete under many meaningful restriction combinations. This in turn also implies the inapproximability of the respective VNEP variants (unless \( P = \mathcal{NP} \) holds). Hence, it is natural to consider a broader class of (approximation) algorithms that may violate constraints by a certain factor: instead of answering the question whether a valid embedding exists that satisfies all capacity constraints, one might for example seek an embedding that uses at most two times the actual capacities. We refer to these embeddings as approximate embeddings:

**Definition 10** (\( \alpha / \beta / \gamma \)-Approximate Embeddings).

A mapping \( m \) is an approximate embedding, if it is valid and violates capacity or latency constraints only within a certain bound. Specifically, we call an embedding \( \alpha / \beta / \gamma \)-approximate, when node and edge allocations are bounded by \( \alpha \) and \( \beta \) times the respective node or edge capacity. Considering latency restrictions, we call a mapping \( \gamma \)-approximate when latencies are within a factor of \( \gamma \) of the original bound. Formally, the following must hold for \( \alpha, \beta, \gamma \geq 1 \):

\[
A_m(u) \leq \alpha \cdot c_S(u) \quad \forall u \in V_S
\]
\[
A_m(u,v) \leq \beta \cdot c_S(u,v) \quad \forall (u,v) \in E_S
\]
\[
\sum_{e \in m_E(i,j)} l_S(e) \leq \gamma \cdot l_r(i,j) \quad \forall (i,j) \in E_r \quad \square
\]

III. REDUCTION FRAMEWORK

This section presents the main insight and contribution of our paper, namely a generic reduction framework that allows to derive hardness results by slightly tailoring the proof for the individual problem variants. Our reduction framework relies on 3-SAT and we first introduce some notation. Afterwards we continue by constructing a (partial) VNEP instance, whose solution will indicate whether the 3-SAT formula is satisfiable.

**A. 3-SAT: Notation and Problem Statement**

We denote by \( \mathcal{L}_\phi = \{x_k\}_{k \in [N]} \) a set of \( N \in \mathbb{N} \) literals and by \( \mathcal{C}_\phi = \{C_i\}_{i \in [M]} \) a set of \( M \in \mathbb{N} \) clauses, in which literals may occur either positively or negated. The formula \( \phi = \bigwedge_{C_i \in \mathcal{C}_\phi} C_i \) is a 3-SAT formula, iff. each clause \( C_i \) is the disjunction of at most 3 literals of \( \mathcal{L}_\phi \). Denoting the truth values by \( F \) and \( T \), 3-SAT asks to determine whether an assignment \( \alpha : \mathcal{L}_\phi \rightarrow \{F, T\} \) exists, such that \( \phi \) is satisfied. 3-SAT is one of Karp’s 21 \( \mathcal{NP} \)-complete problems:

**Theorem 11** (Karp [23]). Deciding 3-SAT is \( \mathcal{NP} \)-complete.

For reducing 3-SAT to VNEP, it is important that the clauses be ordered and we define the following:

**Definition 12** (First Occurrence of Literals). We denote by \( C : \mathcal{L}_\phi \rightarrow [M] \) the function yielding the index of the clause in which a literal first occurs. Hence, if \( C(x_k) = i \), then \( x_k \) is contained in \( C_i \), while not contained in \( C_{i'} \) for \( i' \in [i - 1] \).

As we are interested the satisfiability of a 3-SAT formula \( \phi \), we define the set of satisfying assignments per clause:

**Definition 13** (Satisfying Assignments). We denote by \( A_i = \{a_{i,m} : L_i \rightarrow \{F, T\} \mid a_{i,m} satisfies C_i\} \) the set of all possible assignments of truth values to the literals \( L_i \) of \( C_i \) satisfying \( C_i \). Note that all elements of \( A_i \) are functions.

Lastly, to abbreviate notation, we employ \( L_{i,j} = L_i \cap L_j \) to denote the intersection of the literal sets of \( C_i \) and \( C_j \).

**B. General VNEP Instance Construction**

For a given 3-SAT formula \( \phi \), we now construct a VNEP instance consisting of a substrate graph \( G_{S(\phi)} \) and a request graph \( G_{r(\phi)} \). The question whether the formula \( \phi \) is satisfiable will eventually reduce to the question whether a feasible embedding of \( G_{r(\phi)} \) on \( G_{S(\phi)} \) exists. Figure 2 illustrates the construction described in the following.

**Definition 14** (Substrate Graph \( G_{S(\phi)} \)). For a given 3-SAT formula \( \phi \) we define the substrate graph \( G_{S(\phi)} = (G_{S(\phi)}, E_{S(\phi)}) \) as follows. For each clause \( C_i \in \mathcal{C}_\phi \) and each potential assignment of truth values satisfying \( C_i \), a substrate node is constructed, i.e. we set \( V_{S(\phi)} = \bigcup_{C_i \in \mathcal{C}_\phi} A_i \). We connect two substrate nodes \( a_{i,m} \in V_{S(\phi)} \) and \( a_{i,n} \in V_{S(\phi)} \), iff. a literal \( x_k \) is introduced in the clause \( C_i \) for the first time and is also used in clause \( C_j \), and \( a_{i,m} \) and \( a_{i,n} \) agree on the truth values of the literals contained in both clauses. Formally, we set:

\[
E_{S(\phi)} = \left\{(a_{i,m}, a_{j,n}) \mid \exists x_k \in L_{i,j} \text{ with } C(x_k) = \{i \text{ and } a_{i,m}(x_l) = a_{j,n}(x_l) \text{ for } x_l \in L_{i,j}\} \right\}
\]

Capacities etc. are introduced in the respective reductions.

**Definition 15** (Request Graph \( r(\phi) \)). For a given 3-SAT formula \( \phi \) we define the request graph \( G_{r(\phi)} = (V_{r(\phi)}, E_{r(\phi)}) \) as follows. For each clause \( C_i \in \mathcal{C}_\phi \) a node \( v_i \) is introduced, i.e. \( V_{r(\phi)} = \{v_i \mid C_i \in \mathcal{C}_\phi\} \). Matching the construction of the substrate graph \( G_{S(\phi)} \), we introduce directed edges \( (v_i, v_{i,j}) \in E_{r(\phi)} \) only if there exists a literal \( x_k \in C_i \) being introduced in \( C_i \) and being also used in the clause \( C_j \):

\[
E_{r(\phi)} = \{(v_i, v_{i,j}) \mid \exists x_k \in L_{i,j} \text{ with } C(x_k) = \{i\}\}
\]

Demands etc. are introduced in the respective reductions.
\[ \phi: (x_1 \lor x_2 \lor x_3) \land (x_2 \lor x_4) \land (x_2 \lor x_3 \lor x_4) \]

**C. The Base Lemma**

Nearly all of our results are based on the following lemma.

**Lemma 16.** The 3-SAT formula \( \phi \) is satisfiable if and only if

1) each virtual node \( v_i \) is mapped on a substrate node corresponding to assignments \( A_i \) of the \( i \)-th clause, i.e. \( m_V(v_i) \in A_i \) holds for all \( v_i \in V_{r(\phi)} \), and

2) virtual edges are embedded using a single substrate edge, i.e. \( |m_E(v_i, v_j)| = 1 \) holds for all \( (v_i, v_j) \in E_{r(\phi)} \).

Proof. We first show that if \( \phi \) is satisfiable, then such a mapping \( m \) must exist. Afterwards, we show that if such a mapping \( m \) exists, then \( \phi \) must be satisfiable.

Assume that \( \phi \) is satisfiable and let \( \alpha: L_{\phi} \to \{F,T\} \) denote an assignment of truth values, such that \( \alpha \) satisfies \( \phi \). We construct a mapping \( m = (m_V, m_E) \) for request \( r(\phi) \) as follows. The virtual node \( v_i \in V_{r(\phi)} \) corresponding to clause \( C_i \) is mapped onto the substrate node \( a_{i,m} \in A_i \subseteq V_{S(\phi)} \), i.e. \( a_{i,m} \) agrees with \( \alpha \) on the assignment of truth values to the contained literals, i.e. \( a_{i,m}(x_k) = \alpha(x_k) \) for \( x_k \in C_i \). As \( \alpha \) satisfies \( \phi \), it satisfies each clause and hence \( m_V(v_i) \in V_{S(\phi)} \) holds for all \( C_i \in C_\phi \). The virtual edge \( (v_i, v_j) \in E_{r(\phi)} \) is mapped via the direct edge between \( m_V(v_i) \) and \( m_V(v_j) \). This edge \( (m_V(v_i), m_V(v_j)) \) must exist in \( E_{S(\phi)} \), as the existence of virtual edge \( (v_i, v_j) \) implies that clause \( C_i \) is the first clause introducing a literal of \( L_{i,j} \) and \( m_V(v_i) = a_{i,m} \) and \( m_V(v_j) = a_{j,n} \) must agree by construction on the assignment of truth values for all literals. Clearly, the constructed mapping \( m \) fulfills both the conditions stated in the lemma, hence completing the first half of the proof.

We now show that if there exists a mapping \( m \) meeting the two requirements stated in the lemma, then the formula \( \phi \) is indeed satisfiable. We constructively recover an assignment of truth values \( \alpha: L_{\phi} \to \{F,T\} \) from the mapping \( m \) by iteratively extending the initially empty assignment. Concretely, we iterate over the mappings of the virtual nodes corresponding to clauses \( C_\phi \) one by one (according to the precedence relation of the indices). By our assumption on the node mapping, \( m_V(v_i) \in A_i \) holds. Accordingly, as the substrate node \( m_V(v_j) \) represents an assignment of truth values to the literals of clause \( C_i \), we extend \( \alpha \) by setting \( \alpha(x_k) \equiv [m_V(v_j)](x_k) \) for all literals \( x_k \) contained in \( C_i \).

We first show that this extension is always valid in the sense that previously assigned truth values are never changed. To this end, assume that the clauses \( C_1, C_2, \ldots, C_{i-1} \) were handled without any such violations. Hence the literals \( \bigcup_{j<i} L_j \) have been assigned truth values in the first \( i-1 \) iterations not contradicting previous assignments. When extending \( \alpha \) by the mapping of \( m_V(v_i) \) in the \( i \)-th iteration, there are two cases to consider. First, if none of the literals \( L_i \) were previously assigned a truth value, i.e. \( L_i \cap \bigcup_{j<i} L_j = \emptyset \) holds, then the extension of \( \alpha \) as described above cannot lead to a contradiction. Otherwise, if \( L_{i,pre} = L_i \cap \bigcup_{j<i} L_j \neq \emptyset \) holds, we show that extending \( \alpha \) by \( m_V(v_i) = a_{i,m} \) does not change the truth value of any literal \( x_k \) contained in \( L_{i,pre} \).

For the sake of contradiction, assume that \( x_k \in C_i \) is a literal, for which \( \alpha(x_k) \) does not equal \( [m_V(v_j)](x_k) \). As \( x_k \) was previously assigned a value, there must exist a clause \( C_j \) in which \( x_k \) was first used, such that \( j < i \) holds. Let \( m_V(v_j) = a_{i,m} \in A_i \) and \( m_V(v_j) = a_{j,n} \in A_j \). As the edge \( (v_j, v_i) \) is contained in \( E_{r(\phi)} \) by definition and all edges are mapped using a single substrate edge by our assumptions, \( m_E(v_i, v_j) = \langle (a_j, n, a_{i,m}) \rangle \) must hold. Hence, as \( (a_{j,n}, a_{i,m}) \in E_{S(\phi)} \) must hold and edges are only introduced if assignments agree with each other, we have \( [m_V(v_j)](x_k) = a_{j,n}(x_k) = a_{i,m}(x_k) = [m_V(v_i)](x_k) \). This contradicts our assumption that \( \alpha(x_k) \neq [m_V(v_i)](x_k) \) holds. Hence, the extension of \( \alpha \) is always valid.

By construction of the substrate graph \( G_{S(\phi)} \), the node set \( A_i \subseteq V_{S(\phi)} \) contains only the assignments of truth values for the literals \( L_i \) of clause \( C_i \in C_\phi \) that satisfy the respective clause. Hence, \( \alpha \) satisfies all of the clauses and hence satisfies \( \phi \), completing the proof of the base lemma.

The base lemma is the heart of our reduction framework for obtaining our results and we note that the construction of the substrate and the request graph is polynomial in the size of the 3-SAT formula. Indeed, the base lemma forms the basis for
polynomial-time reductions for the different VNEP decision variants. Concretely, consider some VNEP variant $\langle X | Y \rangle$. If this variant is ‘expressive’ enough such that any feasible embedding must meet the criteria of Lemma 16, then $\langle X | Y \rangle$ is - by reduction from 3-SAT – $NP$-hard. Furthermore, the existence of an Integer Program for each of the VNEP variants (cf. technical report [22]) shows that the variants lie in $NP$ and hence the successful application of the base lemma shows the $NP$-completeness of the respective variants. As a result, for the considered VNEP variants, any optimization problem (e.g. cost) cannot be approximated within any factor. The following lemma formalizes this observation:

**Lemma 17.** If there is a polynomial-time reduction from 3-SAT to the VNEP decision variant $\langle X | Y \rangle$, then the VNEP variant $\langle X | Y \rangle$ is $NP$-complete. Furthermore, any optimization problem over the same set of constraints is (i) $NP$-hard and (ii) inapproximable (within any factor), unless $P = NP$ holds.

Lastly, the following lemma will prove useful when applying the base lemma.

**Lemma 18.** Exactly one of the following two following properties holds for formula $\varphi$:

1) The clauses of $\varphi$ can be ordered such that within the corresponding request graph $G_r(\varphi)$ only the node $v_1 \in V_r(\varphi)$ has no incoming edges.

2) $\varphi$ can be decomposed into formulas $\varphi_1$ and $\varphi_2$, such that the sets of literals occurring in $\varphi_1$ and $\varphi_2$ are disjoint, while $\varphi = \varphi_1 \land \varphi_2$ holds. Hence, $\varphi$ is satisfiable iff. $\varphi_1$ and $\varphi_2$ are (independently) satisfiable.

**Proof.** We prove the statement by a greedy construction and assume that the clauses are initially unordered. We iteratively assign an index to the clauses, keeping track of which clauses were not assigned an index yet. Initially, pick any of the clauses and assign it the index 1. Now, iteratively choose any clause which contains a literal that already occurs in the set of indexed clauses. If no such clause exists, then the clauses already indexed and the clauses not indexed obviously represent a partition of the literal set and hence the second statement holds true. However, if the greedy step succeeded every time, then the following holds with respect to the constructed ordering: any virtual node $v_i$ corresponding to clause $C_i$, for $i > 1$, must have an incoming edge by Definition 15 as the clause overlapped with the already introduced literals.

IV. $NP$-Completeness of the VNEP

We employ our reduction framework outlined in the previous section to derive a series of hardness results for the VNEP. In particular, we first show the $NP$-completeness of the original VNEP variant $\langle VE | * \rangle$ in the absence of additional restrictions. Given this result, we investigate several other problem settings and show, among others, that also deciding $\langle * | LN \rangle$ is $NP$-complete. Hence, even when the physical network does not impose any resource constraints (i.e., nodes and edges have infinite capacities), finding an embedding satisfying latency and node placement restrictions is $NP$-complete. Again, it must be noted that adding further restrictions only renders the VNEP harder (cf. Lemma 9).

**A. $NP$-Completeness under Capacity Constraints**

We first consider the most basic VNEP variant $\langle VE | * \rangle$.

**Theorem 19.** VNEP $\langle VE | * \rangle$ is $NP$-complete and cannot be approximated under any objective (unless $P = NP$).

**Proof.** We show the statement via a polynomial-time reduction from 3-SAT according to Lemmas 16 and 17. Given is a 3-SAT formula $\varphi$. We assume for now that the first statement of Lemma 18 holds, i.e. that within the request graph $G_r(\varphi)$ only the first node $v_1 \in V_r(\varphi)$ has no incoming edge.

To enforce the properties of Lemma 16, we set the substrate and request capacities for some small $\lambda$, $0 < \lambda < 1/|C_\varphi|$, as follows. The capacity of substrate nodes is determined by the clause whose assignments they represent. Furthermore, the capacities decrease monotonically with each clause. Similarly, but now increasing per clause, the capacities of edges are determined by the clause that the edge’s head corresponds to:

\[
c_{s}(a_{i,m}) = 1 + \lambda \cdot (M - i) \quad \forall C_i \in C_\varphi, a_{i,m} \in A_i
c_{s}(e) = 1 + \lambda \cdot i \quad \forall C_i \in C_\varphi, e \in \delta^-(A_i)
\]

The demands are set to match the respective capacities:

\[
c_{r}(v_i) = 1 + \lambda \cdot (M - i) \quad \forall v_i \in V_r(\varphi)
c_{r}(e) = 1 + \lambda \cdot i \quad \forall v_j \in V_r(\varphi), e \in \delta^-(v_j)
\]

Due to the decreasing node demands and capacities, virtual node $v_j \in V_r(\varphi)$ corresponding to clause $C_i$ can only be mapped on substrate nodes $\bigcup_{k=1}^{M} A_k$. Due to the choice of $\lambda$, the capacity of any substrate node is less than 2 while each virtual node has a demand larger than 1. Hence, two virtual nodes can never be collocated (mapped) on the same substrate node. Thus, all virtual edges must be mapped onto at least a single substrate edge. Considering the virtual edge $e = (v_i, v_j) \in E_r(\varphi)$ with demand $c_{r}(e) = 1 + \lambda \cdot j$, the virtual node $v_j$ must be mapped on a substrate node having an incoming edge of at least capacity $1 + \lambda \cdot j$. As the edge capacities increase with the clause index, only the substrate nodes in $\bigcup_{k=1}^{M} A_k$ satisfy this condition. Hence, if node $v_j$ has an incoming edge, it can only be mapped on nodes in $\bigcup_{k=1}^{M} A_k \cap \bigcup_{k=1}^{M} A_k = A_j$. As we assumed that the first statement of Lemma 18 holds for $\varphi$ and hence all nodes $v_2, \ldots, v_M$ have an incoming edge, we obtain that the virtual node $v_i$ must be mapped on $A_i \subseteq V_S(\varphi)$ for $i = 2, \ldots, M$. Considering the first node $v_1$, we observe that only nodes in $A_1$ offer sufficient capacity to host $v_1$. Hence, any feasible embedding will obey the first statement of Lemma 16 regarding the node mappings.

We now show that any feasible mapping will also obey the second property of Lemma 16, namely, that any virtual edge is mapped on exactly one substrate edge. To this end, assume for the sake of contradiction that $(v_i, v_j) \in E_r(\varphi)$ is not mapped on a single substrate edge. As $v_j$ must be mapped...
on some node $a_{i,m} \in A_i$ and $v_j$ must be mapped on some node $a_{j,n} \in A_j$, and as both the request and the substrate are directed acyclic graphs, the mapping of edge $(v_i, v_j)$ must route through at least one intermediate node. Denote by $a_{k,l} \in A_k$ for $i < k < j$ the first intermediate node via which the edge $(v_i, v_j)$ is routed. By construction, the capacity of the substrate edge $(a_{i,m}, a_{k,l})$ is $1 + \lambda \cdot k$. However, as $k < j$ holds and the edge $(v_i, v_j)$ has a demand of $1 + \lambda \cdot j$, the edge $(v_i, v_j)$ cannot be routed via $a_{k,l}$. Hence, the only feasible edges for embedding the respective virtual edges are the direct connections between any two substrate nodes.

Therefore, all feasible solutions indicate the satisfiability of the formula $\phi$. Any algorithm computing a feasible solution to the VNEP obeying node and edge capacities, decides 3-SAT.

Lastly, we argue for the validity of our assumption on the structure of $\phi$, namely that the first statement of Lemma 18 holds. If this were not to hold, then the second statement of Lemma 18 holds true and the formula can be decomposed (potentially multiple times) into disjoint subformulas $\varphi_1, \ldots, \varphi_k$, such that (i) $\varphi = \bigwedge_{i=1}^k \varphi_i$ holds, and (ii) such that the first condition of Lemma 18 holds for each subformula. Accordingly, assuming that an algorithm exists which can construct feasible embeddings whenever they exist, this algorithm can be used to decide the satisfiability of each subformula, hence deciding the original satisfiability problem.

**B. $NP$-completeness under Additional Constraints**

Building on the above $NP$-completeness proof, we can adapt it easily to other settings.

**Theorem 20.** VNEP $\langle E | N \rangle$ is $NP$-complete and cannot be approximated under any objective (unless $\mathcal{P} = \mathcal{NP}$).

**Proof.** In this setting node placement restrictions and substrate edge capacities are enforced. We apply the same construction as in the proof of Theorem 19. Employing the node placement restrictions, we can force the mapping of virtual node $v_i \in V_r(\phi)$ onto substrate nodes $A_i$ by setting $V_{S}^v_i = V_{S}(\phi) \setminus A_i$ for all $v_i \in V_r(\phi)$. By the same argument as before, virtual edges cannot be mapped onto paths as the intermediate nodes do not support the respective demand.

**Theorem 21.** VNEP $\langle V | R \rangle$ is $NP$-complete and cannot be approximated under any objective (unless $\mathcal{P} = \mathcal{NP}$).

**Proof.** In this setting only node capacities must be obeyed, while routing restrictions may be introduced. We employ the same node capacities as in the proof of Theorem 19, such that virtual node $v_i \in V_r(\phi)$ may only be mapped on nodes $\bigcup_{k=1}^j A_k$. Routing restrictions are set to only allow direct edges, i.e. $E_{S}^{v_i,v_j} = E_S(\phi) \setminus (A_i \times A_j)$ holds for each $(v_i, v_j) \in E_r(\phi)$. Again, $v_i \in V_r(\phi)$ must be mapped on a node in $A_j$, while all other virtual nodes have at least one incoming edge according to Lemma 18. As multiple virtual nodes cannot be placed on the same substrate node and virtual edges must span at least one substrate edge, any node $v_j$ can only be mapped on nodes in $A_j$ for $j \in \{2, \ldots, M\}$. Together with the routing restrictions both requirements of Lemma 16 are safeguarded and the result follows.

**Theorem 22.** VNEP variants $\langle \cdot | NR \rangle$ and $\langle \cdot | NL \rangle$ are $NP$-complete and cannot be approximated under any objective (unless $\mathcal{P} = \mathcal{NP}$).

**Proof.** In both cases capacities are not considered at all. Allowing for node placement restrictions, the first property of Lemma 16 is easily safeguarded (cf. proof of Theorem 20). By employing the same routing restrictions as in the proof of Theorem 21 the result follows directly for the case $\langle \cdot | NR \rangle$. Latency restrictions can be easily used to enforce that virtual edges do not span more than one single substrate edge. Concretely, we set unit substrate edge latencies and unit virtual edge latency bounds: if an edge was to be embedded via more than one edge, the latency restrictions would be violated. Hence, the result also holds for $\langle \cdot | NL \rangle$.

**V. $NP$-completeness of Computing Approximate Embeddings**

Given the hardness results presented in Section IV, the question arises to which extent the hardness can be overcome when only computing approximate embeddings (cf. Definition 10), i.e. embeddings that may violate capacity constraints or exceed latency constraints by certain factors. Based on the proofs presented in Section IV, we first derive hardness results for computing $\alpha$-approximate embeddings (allowing node capacity violations) and $\gamma$-approximate embeddings (allowing latency violations). For $\beta$-approximate embeddings (allowing edge capacity violations) a reduction from an edge-disjoint paths problem is presented in our technical report [22].

**Theorem 23.** For $\langle VE|\cdot \rangle$ and $\langle V | R \rangle$ finding an $\alpha$-approximate embedding is $NP$-complete as well as approximable under any objective (unless $\mathcal{P} = \mathcal{NP}$) for any $\alpha < 2$.

**Proof.** Assume that there exists an algorithm computing $\alpha$-approximate embeddings for $\alpha = 2 - \varepsilon$, $0 < \varepsilon < 1$. We adapt the proofs of Theorem 19 and 21 slightly. First, note that for $\alpha$-approximate mappings validity still has to hold according to Definition 10. Hence, by the decreasing node capacities the virtual node $v_j$ can only be mapped on substrate nodes $\bigcup_{k=1}^j A_k$. Furthermore, by either enforcing edge capacities or edge routing restrictions, the node $v_j$ can still only be mapped on $A_j$. Hence, the only missing piece to show that the respective proofs still hold is the fact that still at most a single virtual node can be mapped on a single substrate node. To ensure, that this still holds, we adapt the capacities. Concretely, we choose $\lambda$, such that $\lambda < \varepsilon/(2 \cdot |A_o|)$ holds. Hence, the capacity of any substrate node is less than $1 + \varepsilon/2$. By relaxing the capacity constraints by the factor $2 - \varepsilon$, the allowed substrate node allocations are upper bounded by $(1 + \varepsilon/2) \cdot (2 - \varepsilon) = 2 - \varepsilon - \varepsilon^2/2 < 2$. As the demand of any virtual node is larger than 1, still at most a single virtual node can be mapped on a substrate node. Hence, the respective proofs still apply and the results follow.
The result for $\gamma$-approximate embeddings can be obtained in a very similar fashion.

**Theorem 24.** For $\langle \cdot | NL \rangle$ finding an $\gamma$-approximate embedding is $NP$-complete as well as inapproximable under any objective (unless $P = NP$) for any $\gamma < 2$.

**Proof.** The proof of Theorem 22 relied on the fact that due to the latency constraints each virtual edge must be mapped on a single substrate edge. As the latencies of substrate edges are uniformly set to 1 and all latency bounds are 1 as well, computing a $\gamma$-approximate embedding for $\gamma < 2$ implies that each virtual edge can still only be mapped on a single substrate edge. Hence, the result of Theorem 22 remains valid.

For $\beta$-approximate embeddings similar results can be obtained via a reduction from an edge-disjoint paths problem. Due to space constraints we only state our main result and refer the reader to our technical report for the proof [22].

**Theorem 25.** Finding a $\beta$-approximate embedding for the VNEP variants $\langle VE \cdot \cdot \rangle$ and $\langle E \cdot N \rangle$ is hard to approximate for $\beta \in \Theta(\log n / \log \log n)$, $n = |V|$, unless $NP \subseteq BP\cdot TIME \left( \bigcup_{i=1}^{d} n^{d \log \log n} \right)$ holds.

Note that above $BP\cdot TIME(f(n))$ denotes the class of problems solvable by probabilistic Turing machines in time $f(n)$ with bounded error-probability [24].

**VI. $NP$-COMPLETENESS UNDER GRAPH RESTRICTIONS**

All of our $NP$-completeness results (except Theorem 25) are based on a reduction from 3-SAT, yielding a specific directed-acylic substrate graph $G_{Si(\phi)}$ and a specific directed acyclic request graph $G_{r(\phi)}$ and we note the following.

*Observation* 26. Theorems 19 - 24 still hold when restricting the request and the substrate to acyclic graphs.

Given the hardness of the VNEP and as for example Virtual Clusters (an undirected star network) can be optimally embedded in polynomial time [7], one might ask whether the hardness is preserved when restricting request graphs further.

In this section, we derive the result that the VNEP variants considered in this paper remain $NP$-complete when request graphs are planar and degree-bounded. Our results are obtained by considering a planar variant of 3-SAT. The planarity of a formula $\phi$ is defined according $\phi$’s graph interpretation:

**Definition 27** (Graph $G_{\phi}$ of formula $\phi$). The graph $G_{\phi} = (V_{\phi}, E_{\phi})$ of a SAT formula $\phi$ is defined as follows. $V_{\phi}$ contains a node $v_i$ for each clause $C_i \in \phi$ and a node $u_k$ for each literal $x_k \in L_{\phi}$. An undirected edge $\{v_i, u_k\}$ is contained in $E_{\phi}$ if $x_k$ is contained in $C_i$ (either positive or negative). Note that the graph $G_{\phi}$ is bipartite.

**Theorem 29** ([25]). $4P3C\cdot 3\text{-SAT}$ is $NP$-complete.

The following lemma connects $4P3C\cdot 3\text{-SAT}$ formulas $\phi$ with the corresponding request graphs $G_{r(\phi)}$.

**Lemma 30.** Given a $4P3C\cdot 3\text{-SAT}$ formula $\phi$, the following holds for the request graph $G_{r(\phi)}$ (cf. Definition 15):

1. The request graph $G_{r(\phi)}$ is planar.
2. The node-degree of $G_{r(\phi)}$ is bounded by 12.

**Proof.** We consider an arbitrary $4P3C\cdot 3\text{-SAT}$ formula $\phi$ to which the conditions of Definition 28 apply. We first show that the corresponding request graph $G_{r(\phi)}$ is planar by detailing a transformation process leading from $G_{\phi}$ to $G_{r(\phi)}$ while preserving planarity (see Figure 3 for an illustration).

Starting with the undirected graph $G_{\phi}$, the edges are first oriented: an edge is oriented from a clause node to a literal node iff. the literal occurs in the respective clause for the first time according to the clauses’ ordering. Note that while many reductions in Section IV required the reordering of clause nodes according to Lemma 18, this reordering preserves planarity as the structure of the graph $G_{\phi}$ does not change.

Given this directed graph, the literal nodes are now removed by joining the single incoming edge of the literal nodes with each outgoing edge of the corresponding literal node. In particular, consider the literal node $x_2$ of Figure 3: the single incoming edge $(C_1, x_2)$ is joined with the outgoing edges $(x_2, C_2)$ and $(x_2, C_3)$ to obtain the edges $(C_1, C_2)$.
and \((C_1,C_3)\), respectively. As the duplication of the single incoming edge cannot refute planarity and all incoming and outgoing edges connect to the same node, the planarity of the graph is preserved in this step. Lastly, duplicate edges are removed to obtain the graph \(G_{r(\phi)}\), which is, in turn, planar.

It remains to show, that the request graph \(G_{r(\phi)}\) corresponding to \(\phi\) exhibits a bounded node-degree of 12 (in the undirected interpretation of the graph \(G_{r(\phi)}^e\)). To see this, we note the following: based on the first two conditions of Definition 28, each clause node connects to exactly 3 literal nodes and each literal node connects to at most 4 clause nodes. Hence, when removing the literal nodes in the transformation process, the degree of each node may increase at most by a factor of 4. As any clause node had 3 neighboring literal nodes, this implies that the degree of any node is at most 12 after the transformation process, completing the proof.

Given the above, we easily derive the following theorem:

**Theorem 31.** Theorems 19 - 24 hold when restricting the request graphs to be planar and / or degree 12-bounded. Theorem 25 holds for planar and degree 1-bounded graphs.

**Proof.** Our \(NP\)-completeness proofs in Section IV and Section V (except for Theorem 25) relied solely on the reduction from 3-SAT to VNEP using the base Lemma 16. As formulas of 4P3C-3-SAT are a strict subset of the 3-SAT formulas, the base Lemma 16 is still applicable for 4P3C-3-SAT formulas. However, due to the structure of 4P3C-3-SAT formulas, the corresponding requests in the reductions are planar and exhibit a node-degree bound of 12 by Lemma 30. Hence, solving the VNEP is \(NP\)-complete, even when restricting the requests to planar and / or degree-bounded ones. Lastly, as proven in our technical report [22], Theorem 25 holds for planar and degree 1-bounded requests, concluding the proof.

VII. CONCLUSION

We presented a comprehensive set of hardness results for the VNEP and its variants, which lie at the core of many resource allocation problems in networks. Our results are negative in nature: we show that the problem variants are \(NP\)-complete and hence inapproximable (unless \(P = NP\)) and that this holds true even for restricted classes of request graphs.

We believe that our results are of great importance for future work on several of the virtual network embedding problems. For example, our results on the variant enforcing node placement and latency restrictions are of specific interest for Service Function Chaining. Surprisingly, the respective problem is hard even when not considering any capacity constraints. Furthermore, we have shown that it is hard to compute embeddings satisfying latency bounds within a factor of (less than) two times the original bounds. In turn, whenever latency bounds must be obeyed strictly, one needs to rely on exact algorithmic techniques as e.g. Integer Programming.

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